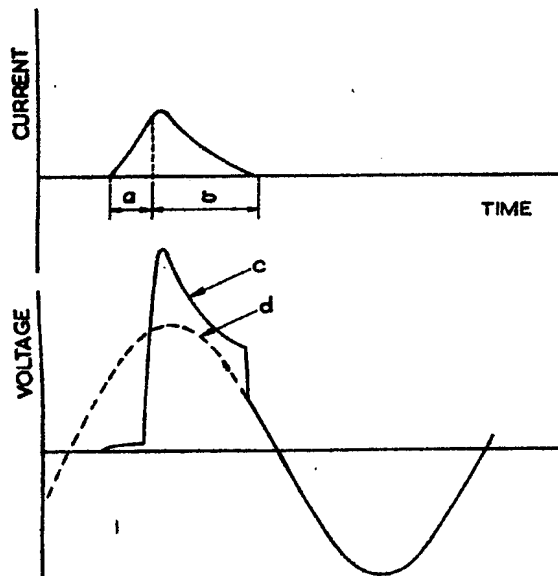


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DRAWINGS



Electrical conditions during short circuit

- a* pre-arcing period
- b* arcing period
- c* fuselink voltage
- d* source e.m.f.

FIG. 1.

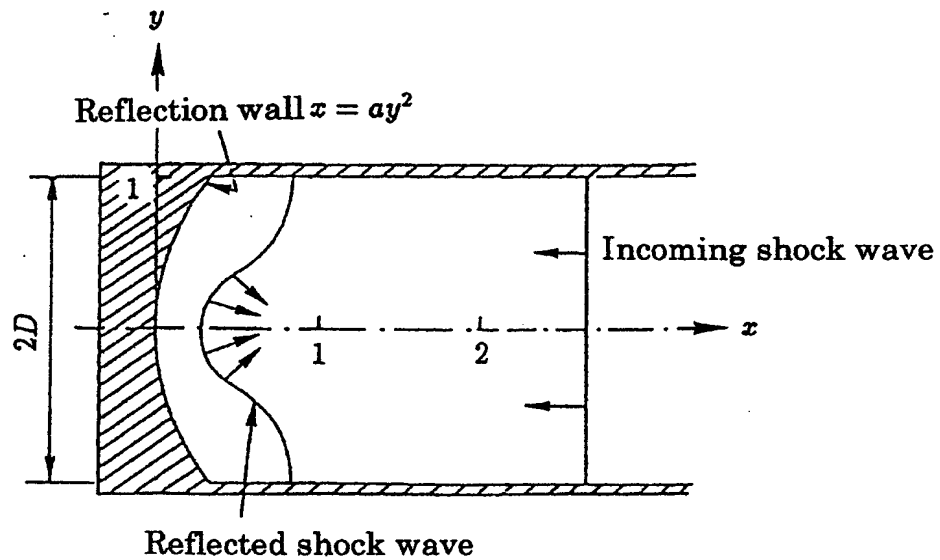


FIG. 2.

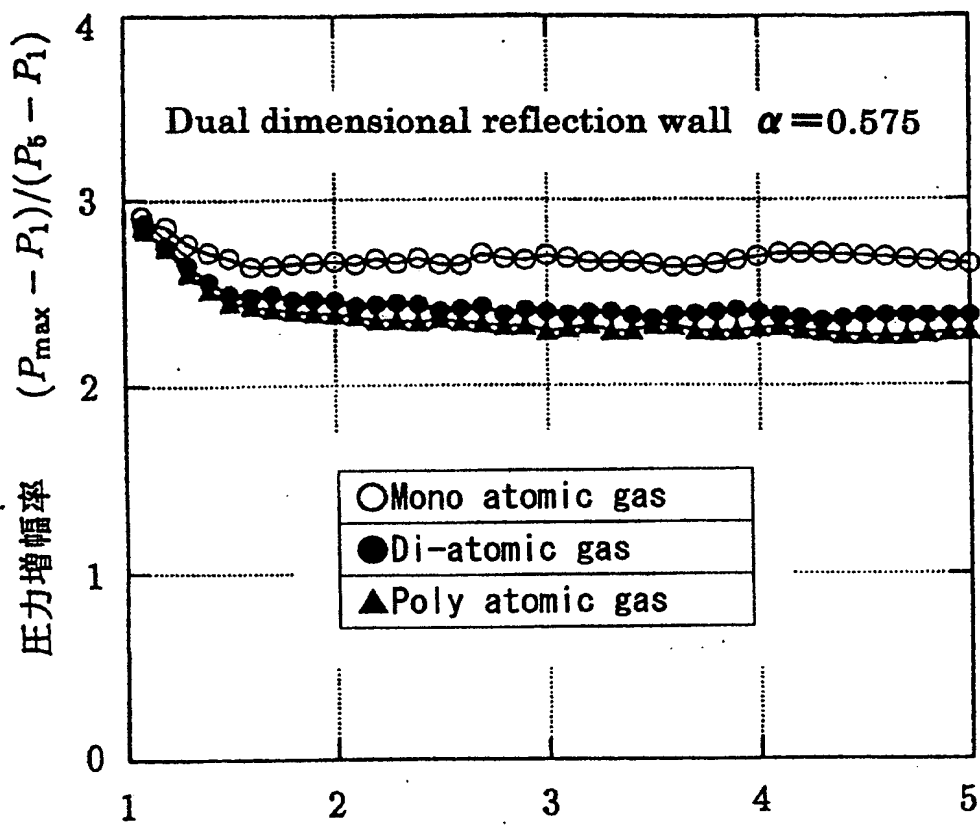


FIG. 3.

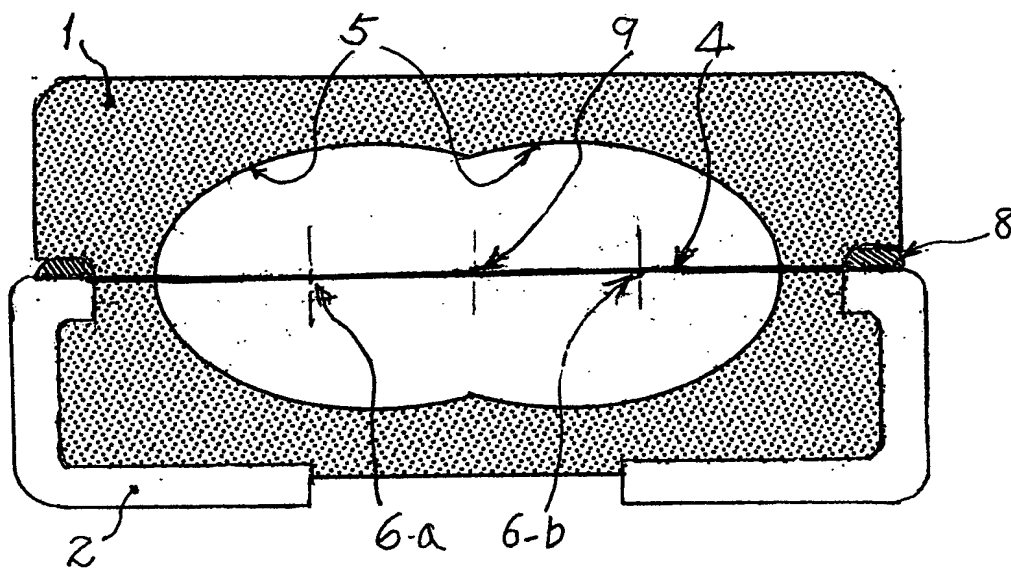


FIG. 4

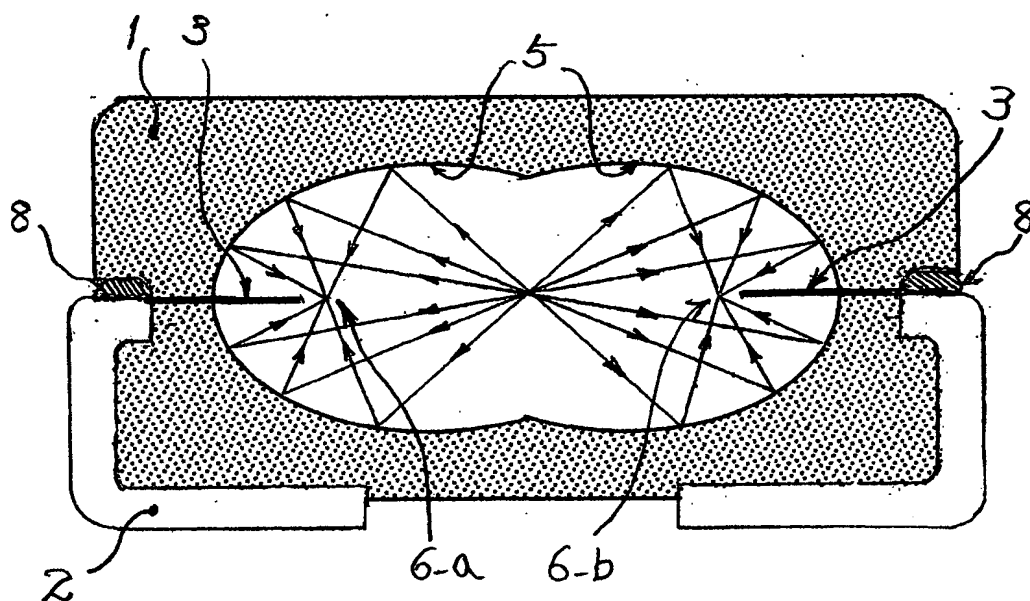


FIG. 5.

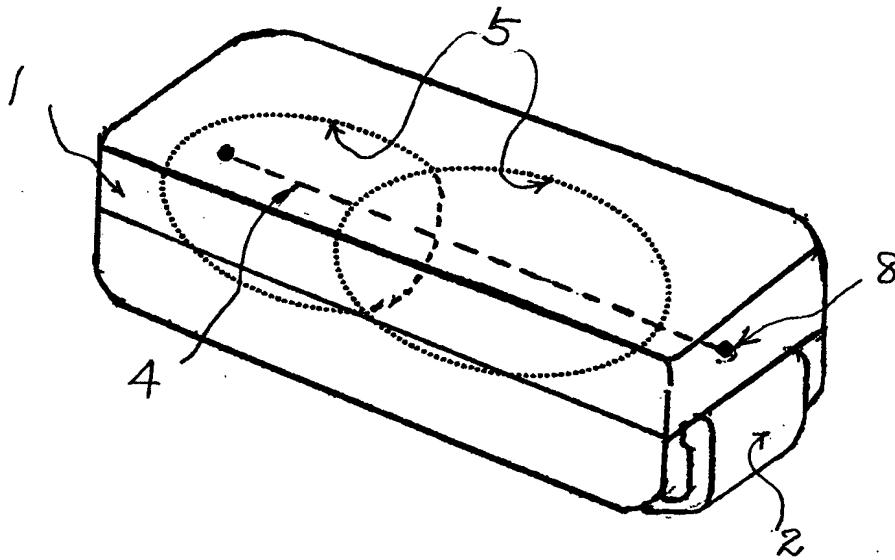


FIG. 6

"New sheet "

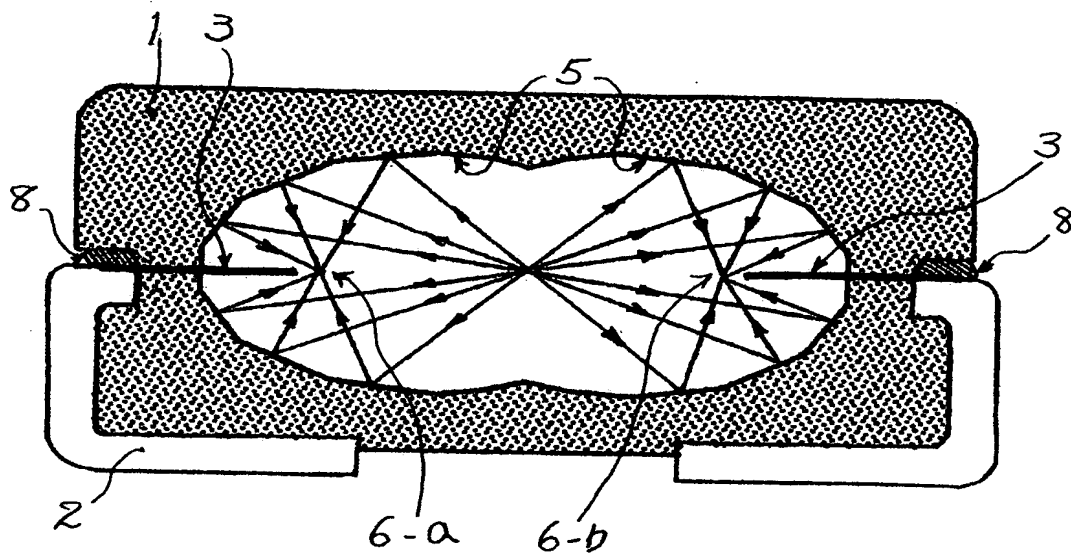


FIG. 7

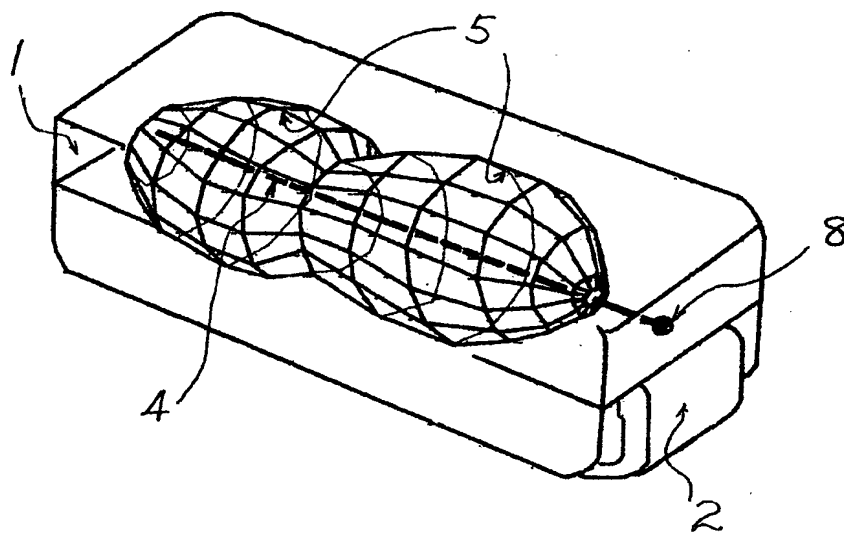


FIG. 8

"Replacement sheet"

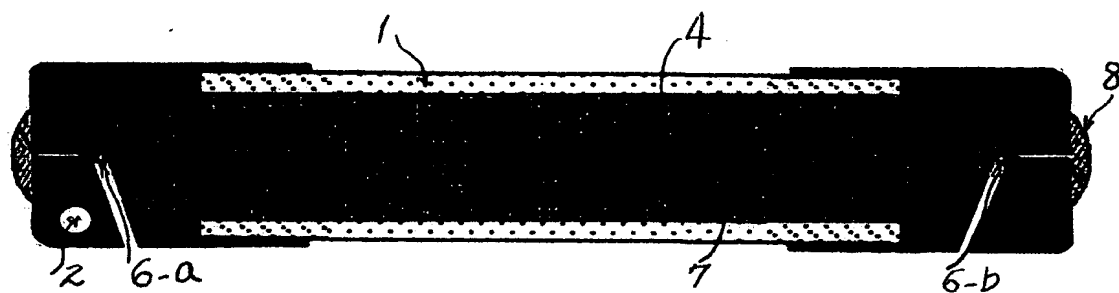


FIG. 9.

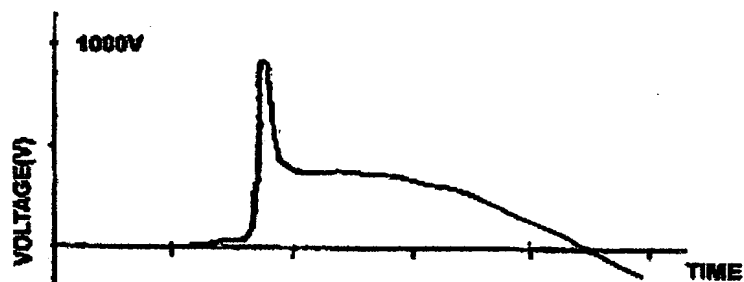
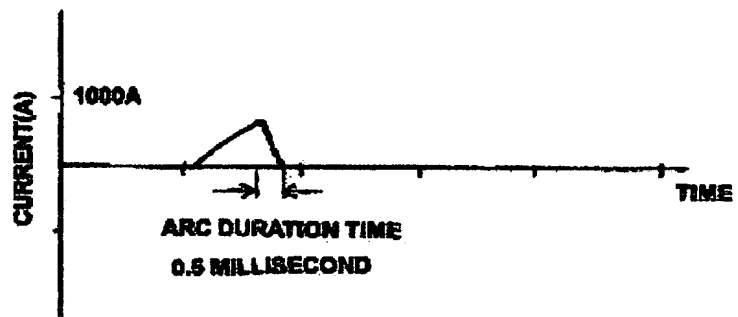


FIG.10.

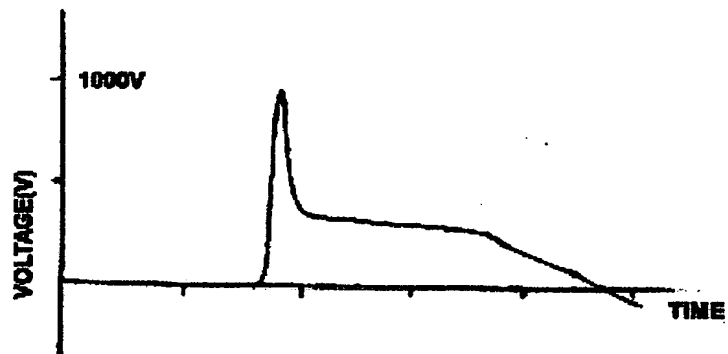
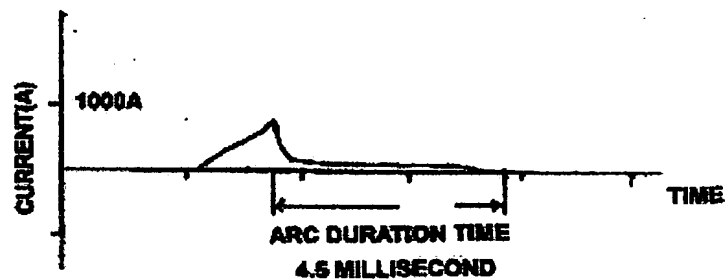


FIG. 11.

APPENDIX

"Annotated sheet"

Previous application

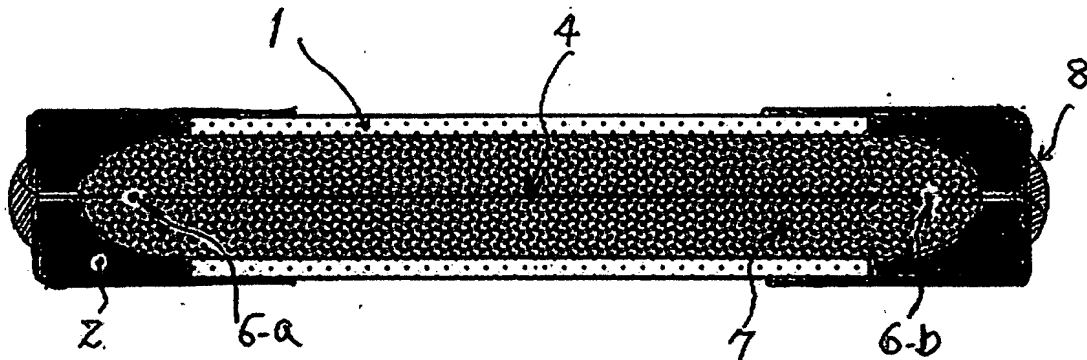


FIG. 7

Currently amended

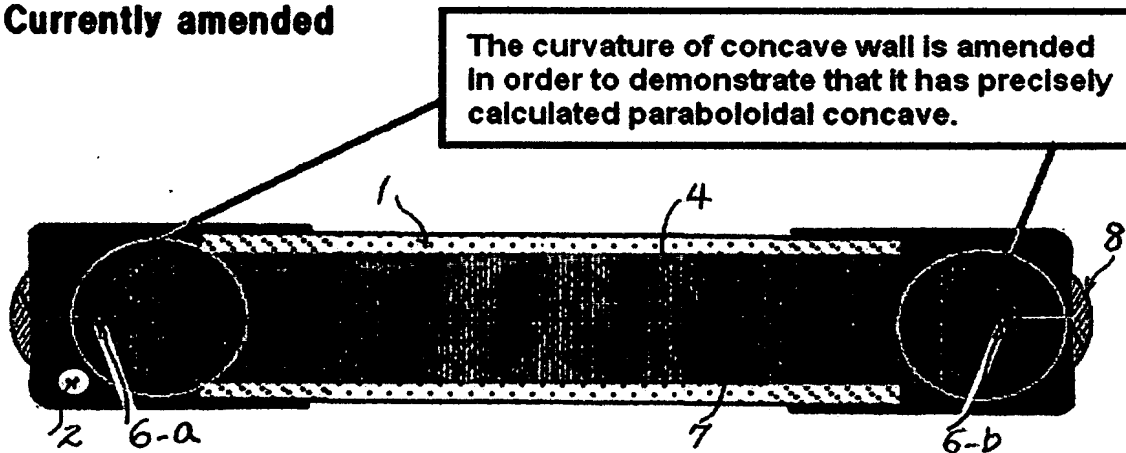


FIG. 9.